

<b>Notice of References Cited</b>	Application/Control No. 09/923,558	Applicant(s)/Patent Under Reexamination HILL ET AL.	
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**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-4,463,443	07-1984	Frankel et al.	364
	B	US-4,888,739	12-1989	Frederick et al.	365
	C	US-5,471,583	11-1995	Au et al.	395
	D	US-5,758,192	05-1998	Alfke	395
	E	US-5,898,893	04-1999	Alfke	395
	F	US-6,263,410	07-2001	Kao et al.	711
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	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
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**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	"Using Select-RAM Memory in XC4000 Series FPGA", 07/7/1996, Xilinx Application note, XAPP 057 Version 1.0, pages 1-2
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\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
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